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|  | |  |  |  |  |  |  |  |  |  |  | | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | | **SL No.** | **Student ID** | **Student Name** | ASSESMENT | QUIZ | VIVA | TOTAL | GRADE | ATTENDANCE | EXPERIMENT | | 1 | 16201137 | Md. Abul Hasan (Retake) |  |  |  |  |  |  | Design and implement MOD-4 UP/down COUNTER and explain working procedure | | 2 | 17201004 | Md. Najmul Islam (Retake) |  |  |  |  |  |  | Design and implement and explain working procedure OF 1 bit AU | | 3 | 18101010 | Dhruba Doti Bhowmick (Regular) |  |  |  |  |  |  | Design and implement and explain working procedure OF 3 bit full adder. | | 4 | 18101052 | Nasir Uddin (Regular) |  |  |  |  |  |  | Design and implement and explain working procedure OF 2 bit full adder | | 5 | 19101001 | Ananna Boiddo (Regular) |  |  |  |  |  |  | Using circuit maker or any circuit simulator Design and implement two-bit logic unit for any two-logic function. Also explain working procedure. | | 6 | 19101002 | Fayza Tabassum Tunisha (Regular) |  |  |  |  |  |  | Design and implement and explain working procedure OF 3 bit full adder. | | 7 | 19101003 | Zarin Tasnim (Regular) |  |  |  |  |  |  | Using circuit maker or any circuit simulator Design and implement two-bit logic unit for any SIX-logic function. Also explain working procedure. | | 8 | 19101004 | Md. Muntasir Shofiq (Regular) |  |  |  |  |  |  | Design and implement 10 bitS SIPO and explain working procedure | | 9 | 19101005 | Rashedul Islam (Regular) |  |  |  |  |  |  | Design and implement 10 bitS PIPO and explain working procedure | | 10 | 19101006 | Khaira Binte Islam Mim (Regular) |  |  |  |  |  |  | Design and implement 10 bitS SISO and explain working procedure | | 11 | 19101007 | Shamima Sultana (Regular) |  |  |  |  |  |  | Design and implement 10 bitS SIPO and explain working procedure | | 12 | 19101008 | Md. Abdur Rashid (Regular) |  |  |  |  |  |  | Using circuit maker or any circuit simulator Design and implement two-bit logic unit for any two-logic function. Also explain working procedure | | 13 | 19101009 | Md. Hasibur Rahman (Regular) |  |  |  |  |  |  | Design and implement 8 bit PIPO and explain working procedure | | 14 | 19101010 | Nor Mohammad Nasim (Regular) |  |  |  |  |  |  | Design and implement 3 bit SIPO and explain working procedure | | 15 | 19101011 | Afroz Jahan Vabna (Regular) |  |  |  |  |  |  | Design and implement 6 bitS SISO and explain working procedure | | 16 | 19101012 | Mehedi Anam (Regular) |  |  |  |  |  |  | Design and implement 5 bit SISO and explain working procedure | | 17 | 19101013 | Tanmoy Mazumder (Regular) |  |  |  |  |  |  | Using circuit maker or any circuit simulator Design and implement 5-bit logic unit for any two-logic function. Also explain working procedure. | | 18 | 19101014 | Twisha Saha Chaudhury (Regular) |  |  |  |  |  |  | Design and implement MOD-4 DOWN COUNTER and explain working procedure | | 19 | 19101015 | Ali Mostakim Alvi (Regular) |  |  |  |  |  |  | Design and implement MOD-4 UP COUNTER and explain working procedure. | | 20 | 19101016 | Tahamid Khan (Regular) |  |  |  |  |  |  | Using circuit maker or any circuit simulator Design and implement two-bit logic unit for any THREE-logic function. Also explain working procedure. | | 21 | 19101019 | Md. Jonayed Sarkar (Regular) |  |  |  |  |  |  | Design and implement 7 bit PIPO and explain working procedure. | | 22 | 19101020 | Shawan Das (Regular) |  |  |  |  |  |  | Using circuit maker or any circuit simulator Design and implement two-bit logic unit for any five-logic function. Also explain working procedure. | | 23 | 19101021 | Md. Tasmim Toha (Regular) |  |  |  |  |  |  | Design and implement 7 bit SISO and explain working procedure. | | 24 | 19101023 | Halima Khatun (Regular) |  |  |  |  |  |  | Design and implement 7 bit SIPO and explain working procedure. | |  |
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